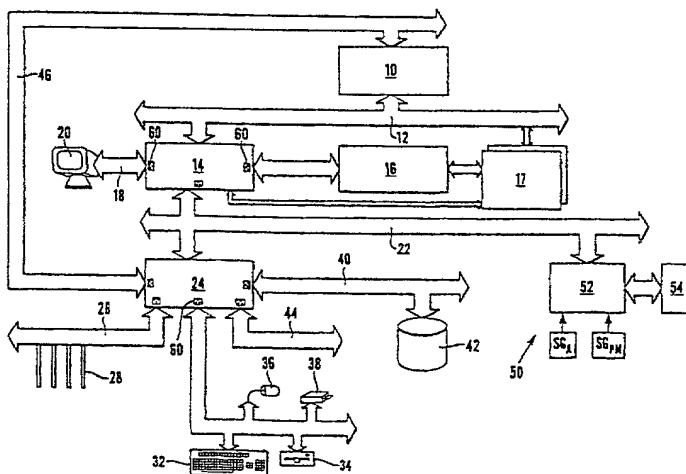




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(54) Title: SYSTEM FOR DATA PROCESSING A SECURITY CRITICAL ACTIVITY



(57) Abstract

The present invention relates to a system for data processing a security critical activity in a secure management mode in a computer, which computer comprises a processor (10), handling devices (20, 28-38), memory storage means (14, 42), hereafter named resources; that the system comprises a security device (50) comprising a processor (52) and signal generators (SG_{PM}, SG_A), a number of control means, hereafter named switches (60), with signal receivers (SR_A, SR_{PM}) arranged respectively between the security device and the pre-selected resources, that the switches contain information regarding accessibility to and from the resources, or parts of the resources, hereafter named resource ranges, wherein the switch controls requests from the computer processor to the resources or resource ranges depending on the information contained in the switch, and wherein, in response to a call from the computer processor or the handling devices, the switches are activated by receiving a signal (SG_{PM}) from the security device, enabling the security device access to and from the resources or resource ranges selected by the security device, and denying the computer processor access to and from the resources or resource ranges selected by the security device.

ABSTRACT

A system for data processing a security critical activity in a secure management mode in a computer, which comprises a processor (10), handling devices (20, 28-38), memory storage resources (14, 42). The system comprises a security device (50) comprising a processor (52) and signal generators (SG_{PM} , SG_A), a number of control switches (60), with signal receivers (SR_A , SR_{PM}) arranged respectively between the security device and the pre-selected resources. The switches contain information regarding accessibility to and from the resources, or parts of the resources, hereafter named resource ranges, wherein the switch controls requests from the computer processor to the resources or resource ranges depending on the information contained in the switch, and wherein, in response to a call from the computer processor or the handling devices, the switches are activated by receiving a signal (SG_{PM}) from the security device, enabling the security device access to and from the resources or resource ranges selected by the security device, and denying the computer processor access to and from the resources or resource ranges selected by the security device.